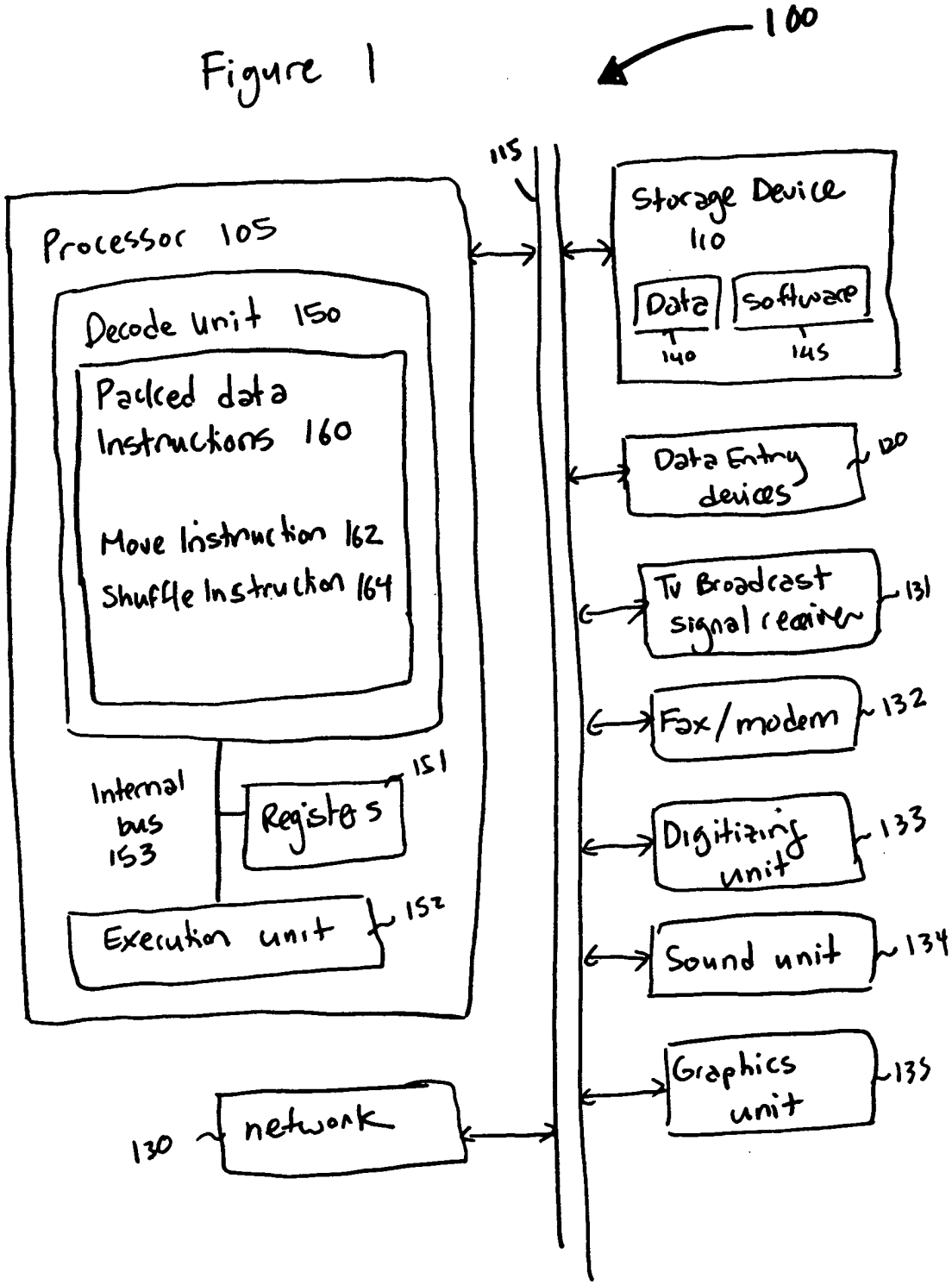
Figure 1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115
Processor 105
Decode Unit 150
Packed data Instructions 160
Move Instruction 162
Shuffle Instruction 164
Internal bus 153
Registers
151
Execution unit
152
Storage Device 110
Data
140
Software
145
Data Entry devices
120
TV Broadcast signal receiver
131
Fax/modem
132
Digitizing unit
133
Sound unit
134
Graphics unit
135
network
130

Figure 2

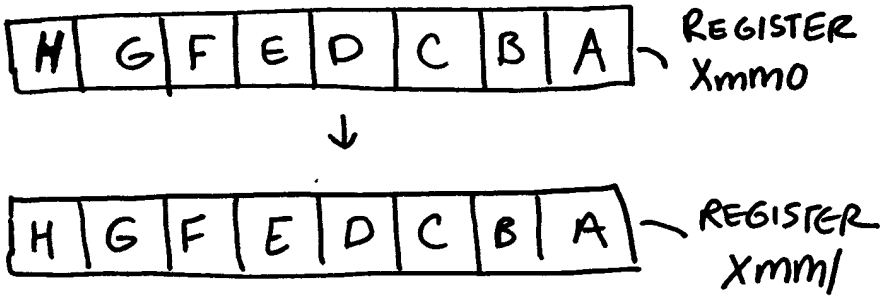


Figure 3A

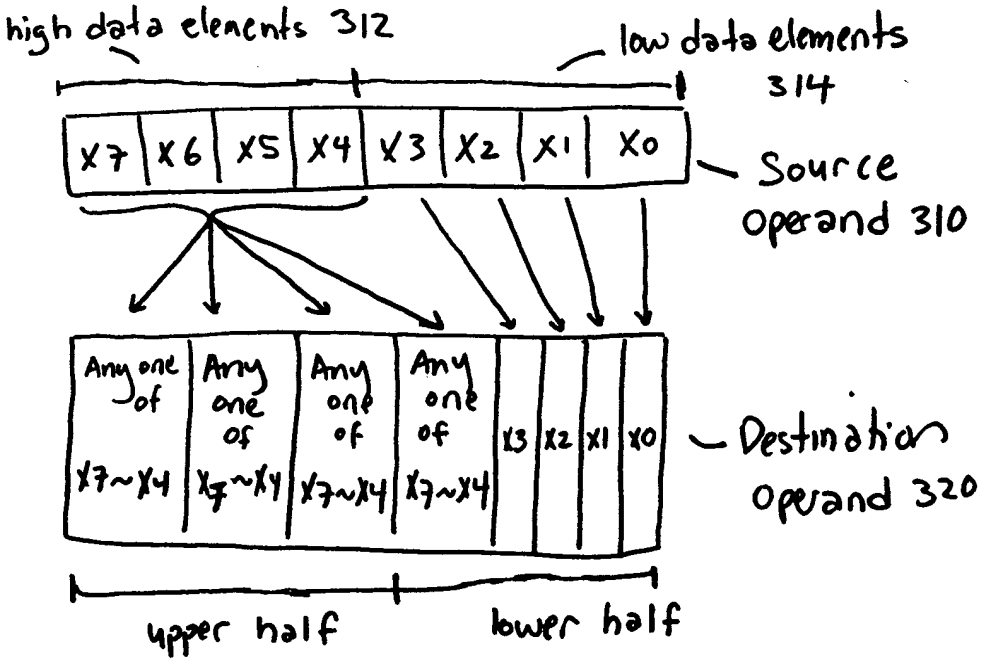


Figure 313

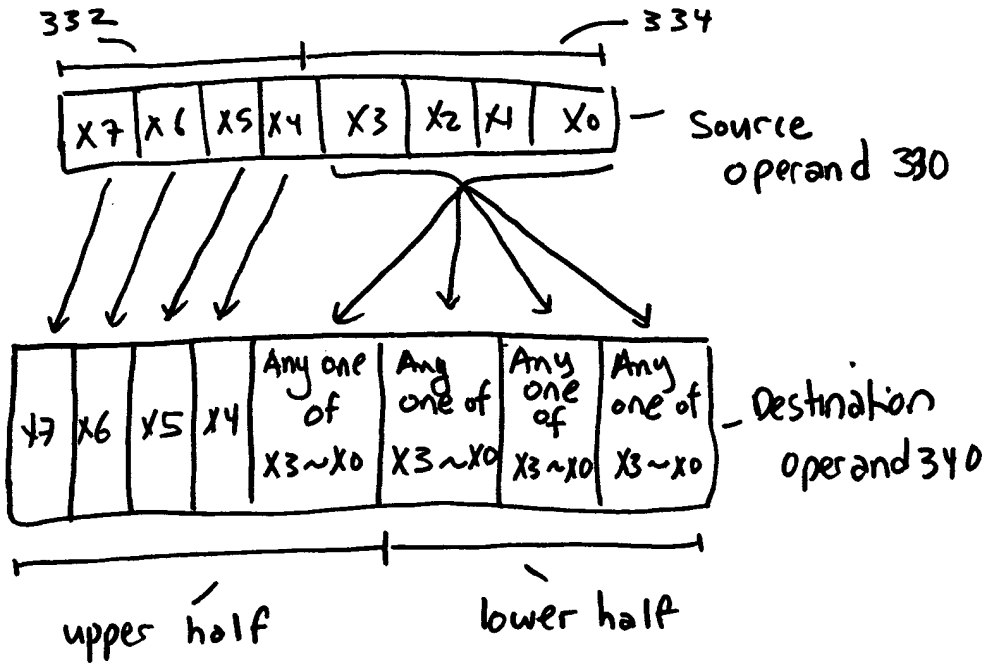
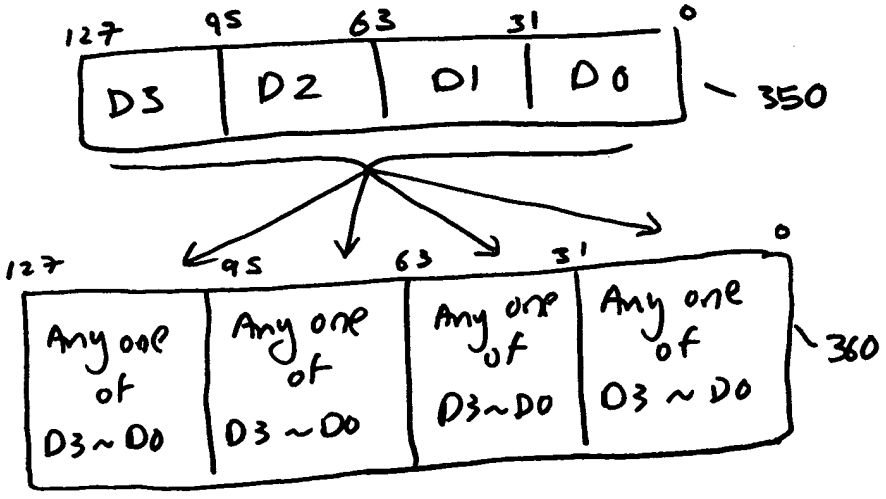


Figure 3C



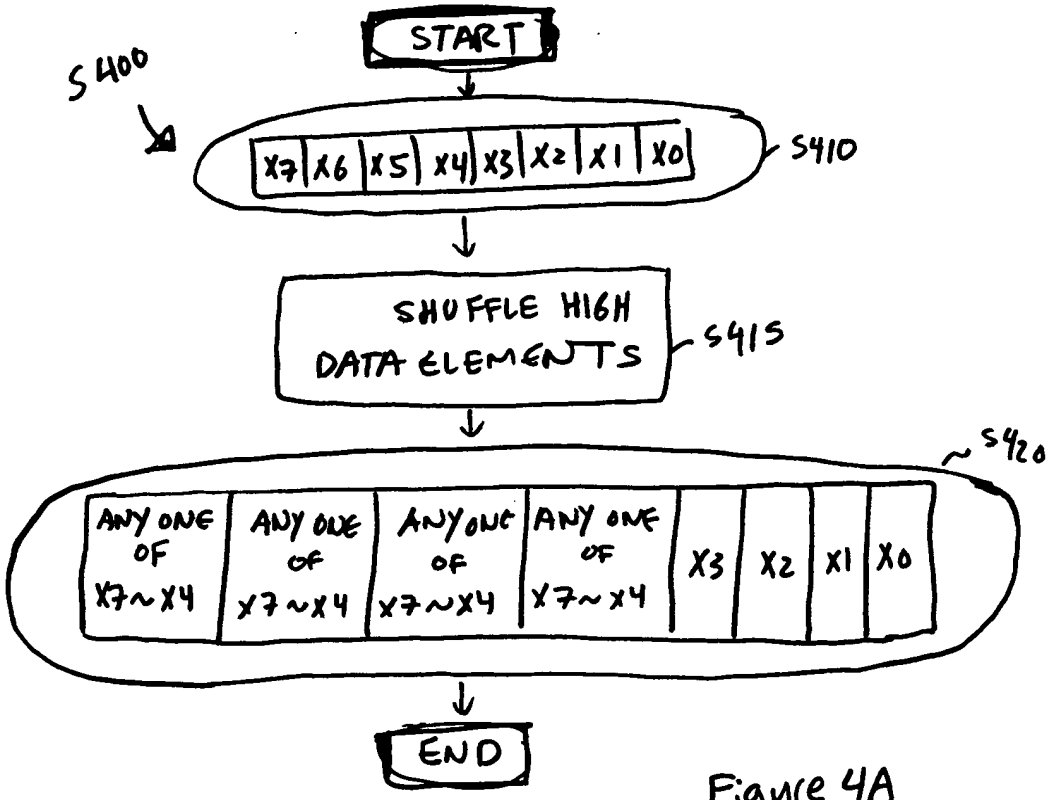


Figure 4A

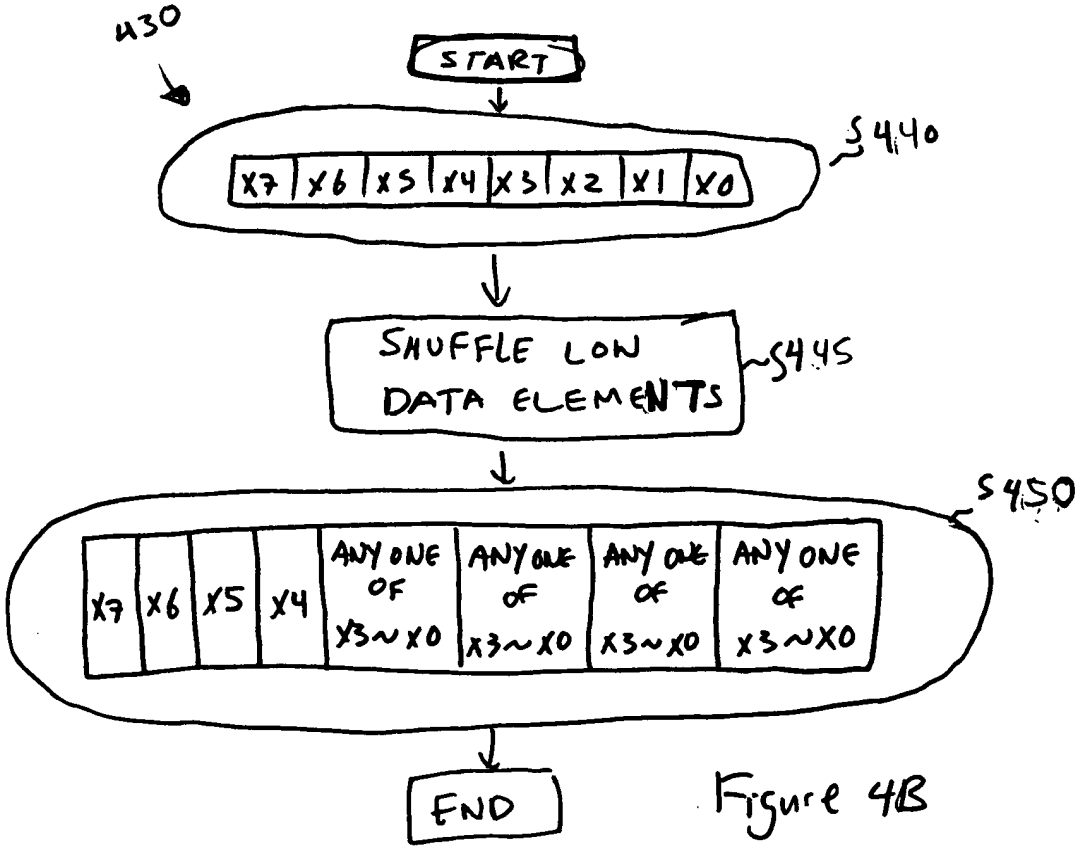


Figure 4B

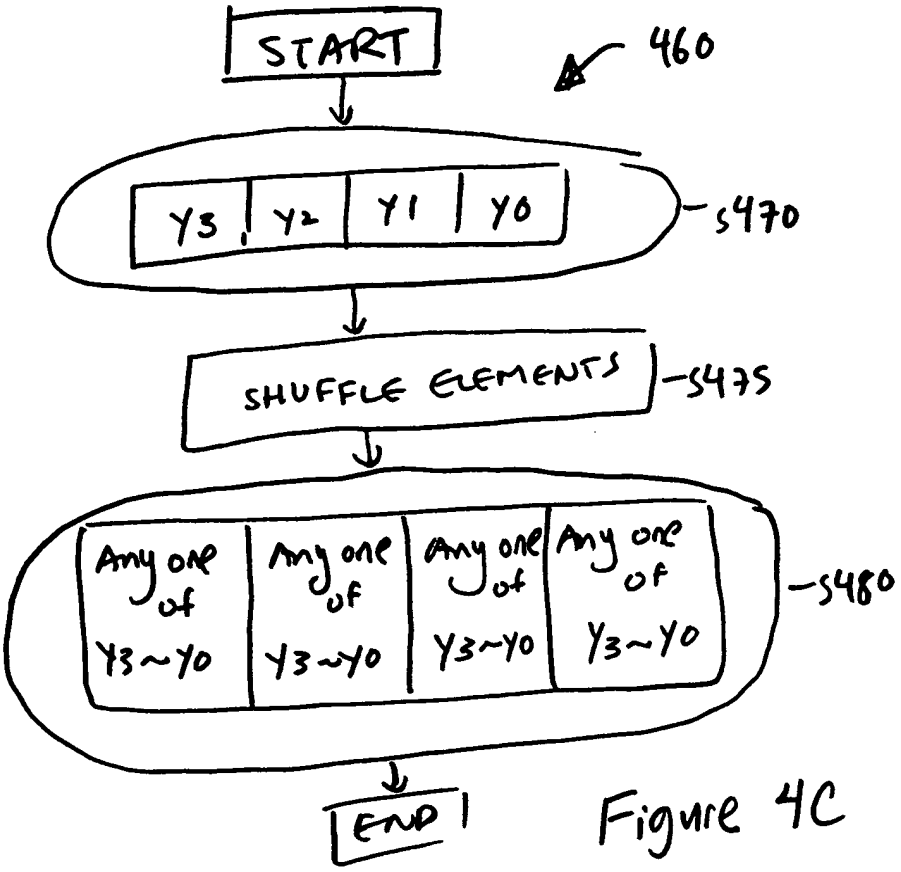


Figure 4C

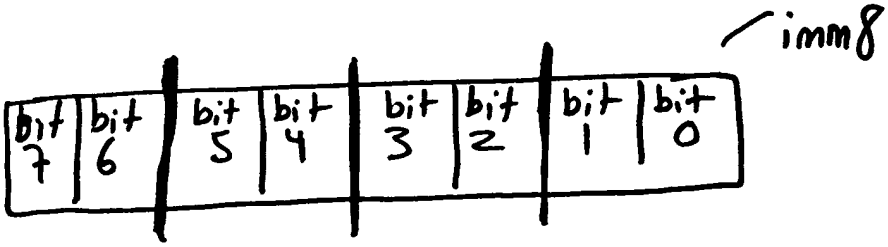


Figure 5

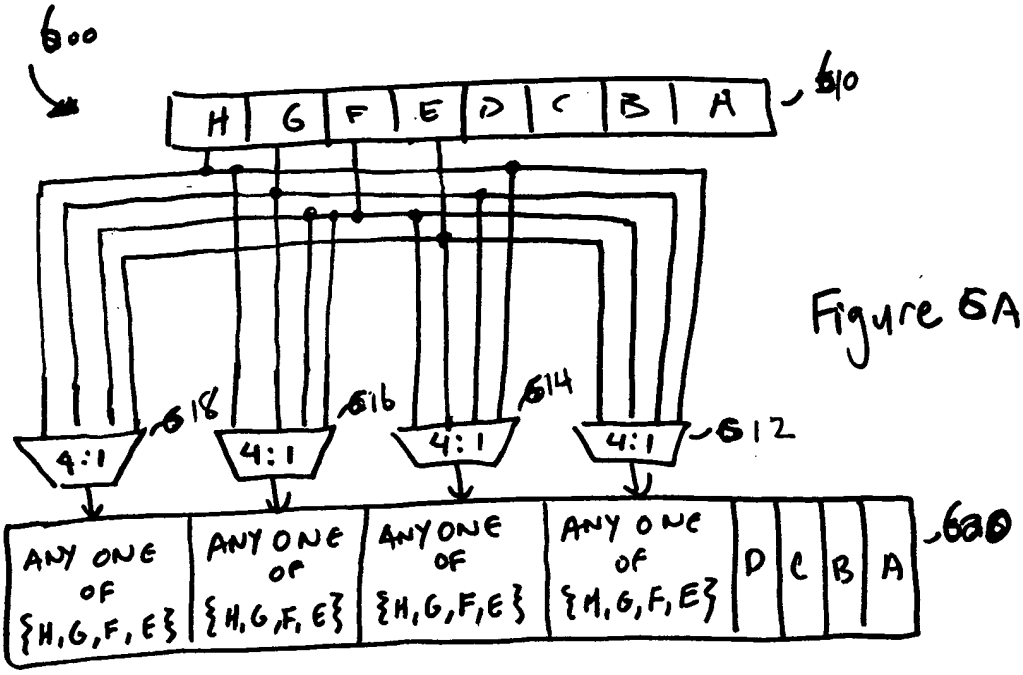


Figure 6A

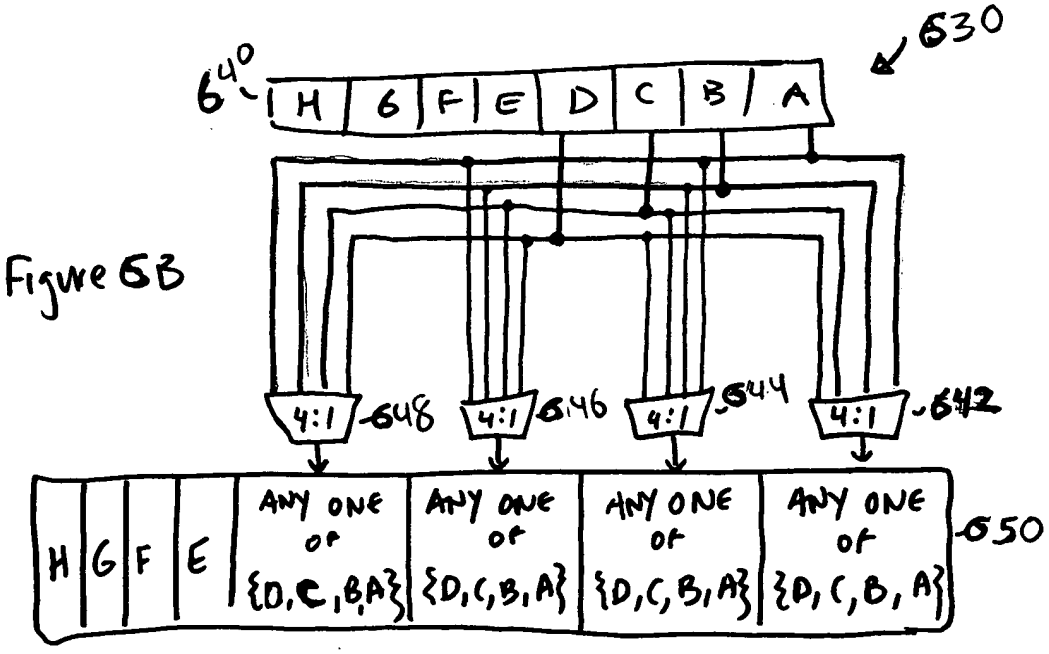


Figure 6B

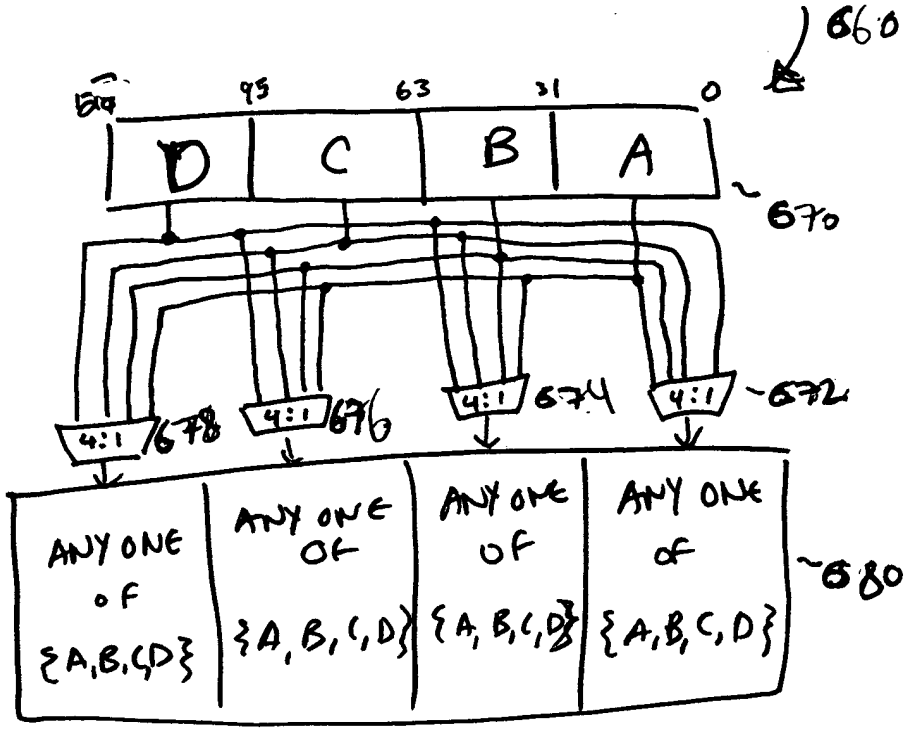

660
95
63
31
0
D
C
B
A
670
4:1
678
4:1
676
4:1
674
4:1
672
ANY ONE of {A,B,C,D}
ANY ONE of {A,B,C,D}
ANY ONE of {A,B,C,D}
ANY ONE of {A,B,C,D}
680

Figure 6C

PATENT 022288.00

Figure 7

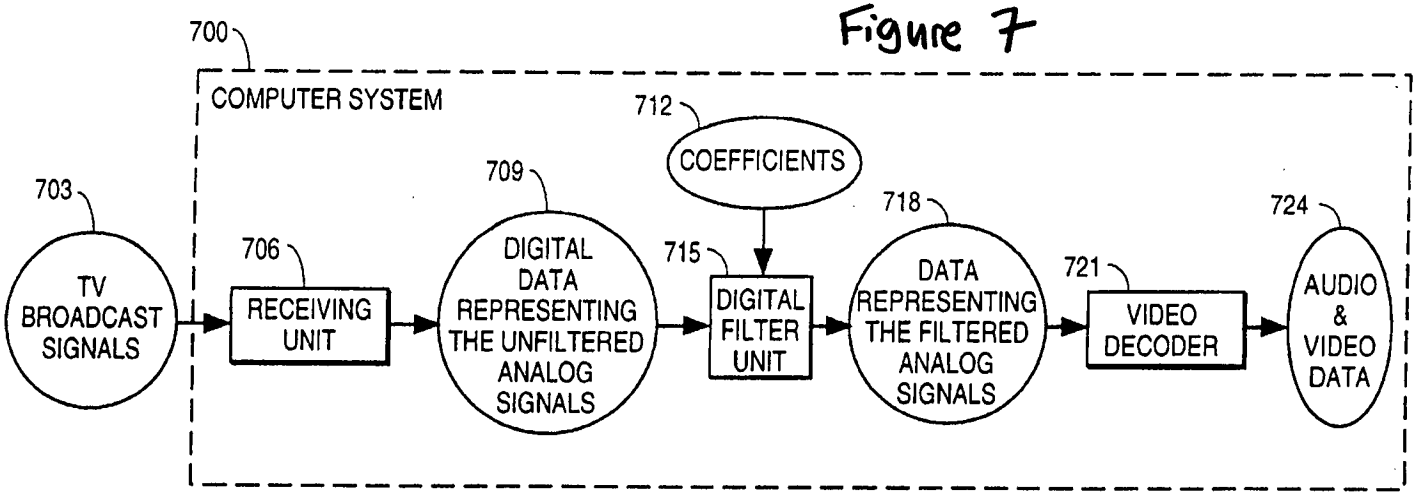
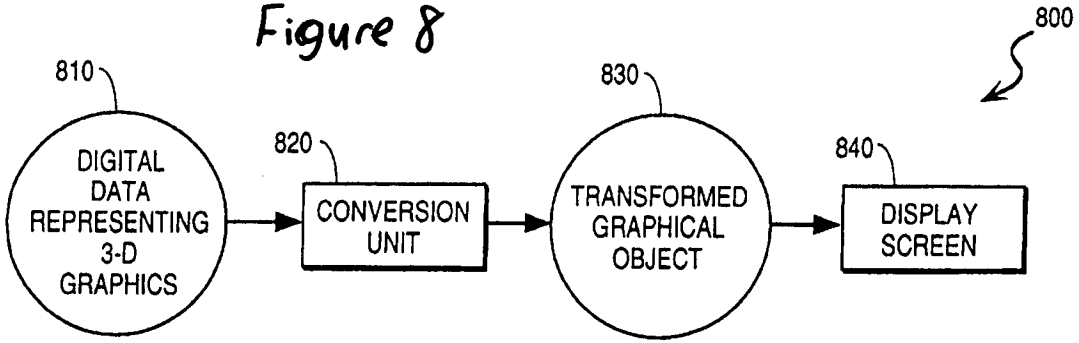



Figure 8